

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 53080
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Norishige NANAI, et al.	:	Confirmation Number: 2140
	:	
Application No.: 10/564,755	:	Group Art Unit: 2811
	:	
Filed: January 08, 2007	:	Examiner: Cuong Quang Nguyen
	:	
For:	:	FIELD EFFECT TRANSISTOR AND METHOD OF FABRICATING THE SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each Japanese reference was cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the Japanese Office Action is attached for the Examiner's information. Please note that the reference **JP 2002-273741** has been previously cited in an Information Disclosure Statement filed January 17, 2006 in the present application.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:mjb
Facsimile: 202.756.8087
Date: October 21, 2009

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as our correspondence address.**